

# Program

## Industrial deployment of spintronics for a more frugal digital world: opportunities and obstacles?

Thursday 28 November 2024 - World Trade Center in Grenoble

Thursday morning

- **8:30 am - 9:00 am**  
Welcome coffee
- **9:00 am - 9:15 am: MINALOGIC and SYSTEMATIC Innovation Clusters, PEPR SPIN**  
Introduction
- **9:15 am - 9:45 am: Lucian Prejbeanu, SPINTEC**  
Spintronics: Recent Advances and Future Innovation Potential
- **9:45 am - 10:05 am: Simone Bertolazzi, YOLE**  
Navigating the Magnetoresistive Device Landscape: Market and Technology Trends
- **10:05 am - 10:25 am: Lucille Engels, ICAIps**  
IC design experience integrating MRAM/interfacing TMR sensor
- **10:25 am - 10:45 am: Johannes Müller, GlobalFoundries**  
Embedded STT-MRAM – Building Trust and Moving Ahead
- **10:45 am - 11:10 am: Coffee break**
- **11:10 am - 11:30 am: Berthold Ocker, SINGULUS**  
Challenges and Opportunities for equipment manufactures in Europe
- **11:30 am - 11:50 am: Siamak Salimy, HPROBE**  
Advanced testing for spintronic devices
- **11:50 am - 12:10 pm: Olivier Faynot, CEA-LETI**  
European Pilot Lines within Chips Act strategy: Role of spintronics
- **12:10 am - 12:25 pm: Jeremy Perret, CIME-P**  
Multi Project Wafer (MPW) Prototyping Centre

**Lunch - 12:25 am - 2:00 pm**

Thursday afternoon

- **2:00 pm - 2:10 pm: Hervé Martin, Chief of the Department of Mathematics, Physics, Nanosciences, and ICT - MESR**  
Government expectations in spintronics and frugal electronics (in French)
- **2:10 pm - 2:30 pm: Mihai Miron, Golana Computing**  
A new AI solution for Asset Diagnostic in Industrial Environment
- **2:30 pm - 2:50 pm: Ali Alaoui, Allegro MicroSystems**  
TMR Risks and Opportunities: How Europe is leading R&D but missing market share
- **2:50 pm - 3:10 pm: Johannes Paul, SENSITEC**  
Magnetoresistive sensors: Existing applications and new trends
- **3:10 pm - 3:30 pm: Jean-Philippe Attané, Nellow**  
Ferroelectric spintronic components for ultralow power logic and AI
- **3:30 pm - 3:40 pm: Marko Erman, Paolo Bortolotti, THALES**  
Defense and security: Low-energy architectures based on novel enabling technologies for embedded applications in the defense domain: opportunities for spintronics
- **3:50 pm - 4:35 pm: Round Table**  
What are the obstacles to the development of national industrial champions in spintronics and what can be done to help them emerge? (in French)
- **4:35 pm - 4:40 pm: Conclusion**